

Notice of Allowability

Application No.

10/667,234

Examiner

PHAN T.H. PALMER

Applicant(s)

COLGAN ET AL.

Art Unit

2874

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 09/17/2003.
2. ☒ The allowed claim(s) is/are 1-20.
3. ☒ The drawings filed on 17 September 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Phan T. H. Palmer
PHAN T. H. PALMER
PRIMARY EXAMINER

05/05/2005

REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance: the claims are allowable over prior art of record, because none of the references in alone or in combination discloses:

An optoelectronic assembly for an electronic system comprising a support electronic chip set; a first substrate; a second substrate; an optoelectronic transducer; and an optical fiber array aligned at first end with the optoelectronic transducer and a second end with an optical signaling medium, as discloses in claims 1, and 11.

A method of fabricating an optoelectronic assembly for communicating a signal from a system electronic chip comprising: etching wells an optoelectronic transducer on a Si carrier; wiring the support electronic chip set and the optoelectronic transducer on the combination of the ceramic carrier and the Si carrier; interconnecting electrically support electronic chip set to the optoelectronic transducer; bonding a fiber array aligned with the optoelectronic transducer; and aligning an optical signaling medium to the fiber array. (as discloses in claim 16).

The underlined limitations of above claims is critically, since the device and method of the optoelectronic assembly is improved: reduced thermal resistance thereby maintaining low temperatures for improved reliability; reduced the signal loss and distortion in the electrical transport media; and significant cost and complexity reduction by decreasing the number of signal lines between

processor complexes, minimizes the need to de-skew the parallel signals, and provides inherent Electromagnetic Compatibility (EMC).

The Colgan et al. (6,752,539 B2) is closest reference discloses apparatus and system for providing optical bus interprocessor interconnect or comprising a multichip module and optical module, but this reference does not disclose the significant improves limitation (underlined limitations) of the present invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

CONTACT INFORMATION

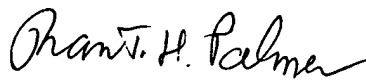
2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to PHAN T.H. PALMER whose telephone number is (571) 272-2354. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RODNEY B. BOVERNICK can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PTHP
05/05/2005


PHAN T. H. PALMER
PRIMARY EXAMINER